

Notice of References Cited

Application/Control No.

09/146,839

Applicant(s)/Patent Under
Reexamination
SRINIVASAN ET AL.

Examiner

Anh D. Mai

Art Unit

2814

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf et al., Silicon Processing for the VLSI Era. Vol 1, 1986, Lattice Press, pp. 162-163 and 173.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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